

DSPenabledRadio 2003
Livingston, Scotland, UK Colloquium Event Report
www.DSPenabledRadio.org

The DSPenabledRadio event took place in Livingston, Scotland, UK in September 2003. The event was sponsored by the IEE, and cosponsored by EURASIP, the IEEE Comms Chapter (UK) and the Institute of System Level Integration (ISLI).

Overall the event was a great success and with more 160 attendees on both days. More than 50 papers were submitted covering a range of topics in DSP and software enabled radio. The first day featured the plenary session "*The Evolving DSP Roadmap for Software Defined Radio*" presented by Walter Tuttlebee of the Mobile Virtual Centre of Excellence and some DSP enabled Radio tutorial sessions. On the second day there were two parallel lecture sessions and a ½ day poster sessions. A complete digest of the colloquium was printed (323) pages, and also produced on CDROM. The event also featured a small exhibition by industry and there were industry stands from Xilinx, Altera, Entegra, Mobile VCE, QinetiQ, Elektrobit, Nallatech, Pentland Systems, Alpha-Data and Epson.

The colloquium had an excellent breadth and coverage of this exciting market. Most noticeably a number of submissions discussed FPGA rapid prototyping for digital radio, and focussed on the design flows and tools required, as well as on implementation of traditional and novel algorithms. In all 12 technical sessions were run under the headings

- Transceiver Design and Architectures
- FPGA and Reconfigurable Hardware Solutions
- Software Defined Radio Architectures
- Software Defined Radio Implementation/design/systems
- DSP Enabled Radio Algorithms
- Sigma-delta Data Converters
- Smart Antenna Architectures And Algorithms
- Smart Antenna Implementations
- Channel Modelling
- Design Tools
- WLAN-protocols
- Middleware

A full listing of the papers can be found at: <http://www.DSPenabledRadio.org>. The technical papers and contributions came from a wide variety of industry and academia including:

Industry: Altera Ltd, Amphion Ltd, Austrian Research Centre, Cambridge Consultants Ltd, Elektrobit Ltd, Entegra Ltd, Epigon Media Technologies, Institute of System Level Integration, Xilinx Ltd, Kyocera Corporation, Nallatech Ltd, Panasonic Mobile Communication, Pentland Systems Ltd, Philips Semiconductors, Picochip Ltd, QinetiQ Ltd, RF Engines Limited, Samsung Electronics Research Institute, Sandbridge Technologies Inc, Thales Communications Ltd, Ubinetics Ltd.

Academic: Universities of Aberdeen, Duisburg-Essen, Durham, Edinburgh, Karlsruhe, Liverpool, Lancaster, Manchester, Mondragon, King's College, ENST-Bretagne, Nebraska, Newcastle, Orleans, Sheffield, Southampton, Strathclyde, Trinity College, Warwick.

DSPenabledRadio 2005 is planned for September 2005, and will be hosted at the University of Southampton, UK with Stephan Weiss (sw1@ecs.soton.ac.uk) being the General Chair. Full details will appear shortly on the website at <http://www.DSPenabledRadio.org>. We look forward to seeing you there!

Available Proceedings: Some copies of the CDROM proceedings are still available at a cost of £25 (or \$40, Euros40). Please contact the conference chair (chair@dspenabledradio.org) directly to order a copy.

Bob Stewart (University of Strathclyde)
General Chairman, **DSPenabledRadio 2003**